

# COMPUTER LOGIC GATES

**COMPUTER LOGIC GATES** ARE THE FUNDAMENTAL BUILDING BLOCKS OF ALL DIGITAL ELECTRONICS, INCLUDING THE SOPHISTICATED COMPUTERS WE USE DAILY. UNDERSTANDING THESE SIMPLE YET POWERFUL COMPONENTS IS CRUCIAL FOR ANYONE INTERESTED IN HOW COMPUTERS PROCESS INFORMATION. THIS ARTICLE WILL DELVE DEEP INTO THE WORLD OF COMPUTER LOGIC GATES, EXPLAINING WHAT THEY ARE, THEIR CORE FUNCTIONS, AND HOW THEY ARE COMBINED TO CREATE COMPLEX DIGITAL CIRCUITS. WE WILL EXPLORE THE BASIC TYPES OF LOGIC GATES, SUCH AS AND, OR, AND NOT, AND THEN MOVE ON TO MORE COMPLEX ONES LIKE XOR AND NAND. FURTHERMORE, WE WILL DISCUSS THEIR REAL-WORLD APPLICATIONS AND THE FOUNDATIONAL ROLE THEY PLAY IN DIGITAL COMPUTING. PREPARE TO UNRAVEL THE INTRICATE LOGIC THAT POWERS OUR TECHNOLOGICAL WORLD.

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# WHAT ARE COMPUTER LOGIC GATES?

COMPUTER LOGIC GATES ARE ELECTRONIC CIRCUITS THAT PERFORM BASIC LOGICAL OPERATIONS ON ONE OR MORE BINARY INPUTS AND PRODUCE A SINGLE BINARY OUTPUT. IN ESSENCE, THEY ARE THE DECISION-MAKERS WITHIN ANY DIGITAL SYSTEM. A BINARY INPUT CAN ONLY HAVE TWO POSSIBLE STATES: 0 (REPRESENTING FALSE OR LOW VOLTAGE) OR 1 (REPRESENTING TRUE OR HIGH VOLTAGE). LOGIC GATES MANIPULATE THESE BINARY VALUES ACCORDING TO SPECIFIC RULES, FORMING THE FOUNDATION FOR ALL DIGITAL COMPUTATIONS AND DATA PROCESSING. THEIR DESIGN AND FUNCTIONALITY ARE ROOTED IN BOOLEAN ALGEBRA, A MATHEMATICAL SYSTEM THAT DEALS WITH BINARY VARIABLES AND LOGICAL OPERATIONS.

## THE BASICS OF BOOLEAN ALGEBRA

BOOLEAN ALGEBRA, NAMED AFTER MATHEMATICIAN GEORGE BOOLE, PROVIDES THE THEORETICAL FRAMEWORK FOR UNDERSTANDING COMPUTER LOGIC GATES. IT IS A SYSTEM OF ALGEBRA WHERE VARIABLES CAN ONLY TAKE ON TWO VALUES, TYPICALLY REPRESENTED AS 0 AND 1, OR FALSE AND TRUE. THE FUNDAMENTAL OPERATIONS IN BOOLEAN ALGEBRA ARE AND, OR, AND NOT, WHICH DIRECTLY CORRESPOND TO THE PRIMARY LOGIC GATES. THESE OPERATIONS ARE DEFINED BY TRUTH TABLES, WHICH SYSTEMATICALLY LIST ALL POSSIBLE INPUT COMBINATIONS AND THEIR CORRESPONDING OUTPUTS. MASTERING THESE BASIC OPERATIONS IS KEY TO GRASPING HOW MORE COMPLEX DIGITAL CIRCUITS FUNCTION.

## FUNDAMENTAL LOGIC GATES AND THEIR FUNCTIONS

THE THREE MOST FUNDAMENTAL LOGIC GATES ARE THE AND, OR, AND NOT GATES. THESE ARE THE BUILDING BLOCKS UPON WHICH ALL OTHER DIGITAL LOGIC CIRCUITS ARE CONSTRUCTED. EACH GATE HAS A DISTINCT FUNCTION BASED ON ITS NAME AND HOW IT PROCESSES ITS INPUTS.

### THE AND GATE

AN AND GATE OUTPUTS A HIGH (1) ONLY IF ALL OF ITS INPUTS ARE HIGH. IF ANY INPUT IS LOW (0), THE OUTPUT WILL BE LOW. THIS IS ANALOGOUS TO THE LOGICAL CONJUNCTION "AND" IN EVERYDAY LANGUAGE. FOR INSTANCE, IF YOU NEED TO TURN ON A LIGHT AND BOTH SWITCH A AND SWITCH B MUST BE CLOSED, THE LIGHT ONLY TURNS ON WHEN BOTH SWITCHES ARE ENGAGED. THE TRUTH TABLE FOR A TWO-INPUT AND GATE SHOWS THAT THE OUTPUT IS 1 ONLY WHEN BOTH INPUTS ARE 1.

### THE OR GATE

AN OR GATE OUTPUTS A HIGH (1) IF AT LEAST ONE OF ITS INPUTS IS HIGH. IT ONLY OUTPUTS A LOW (0) IF ALL OF ITS INPUTS ARE LOW. THIS MIRRORS THE LOGICAL DISJUNCTION "OR." IMAGINE A SCENARIO WHERE A DOOR OPENS IF EITHER BUTTON A OR BUTTON B IS PRESSED; THE DOOR WILL OPEN IF ONE OR BOTH BUTTONS ARE PRESSED. THE TRUTH TABLE FOR A TWO-INPUT OR GATE INDICATES THAT THE OUTPUT IS 0 ONLY WHEN BOTH INPUTS ARE 0.

## THE NOT GATE (INVERTER)

A NOT GATE, ALSO KNOWN AS AN INVERTER, IS UNIQUE BECAUSE IT HAS ONLY ONE INPUT AND ONE OUTPUT. IT SIMPLY INVERTS THE INPUT SIGNAL. IF THE INPUT IS HIGH (1), THE OUTPUT IS LOW (0), AND VICE VERSA. THIS IS A CRITICAL OPERATION FOR NEGATING A LOGICAL STATE. FOR EXAMPLE, IF A CONDITION IS TRUE, A NOT GATE CAN MAKE IT FALSE, OR IF IT'S FALSE, IT CAN MAKE IT TRUE.

## DERIVED LOGIC GATES

WHILE AND, OR, AND NOT GATES ARE FUNDAMENTAL, SEVERAL OTHER LOGIC GATES ARE DERIVED FROM COMBINATIONS OF THESE BASIC ONES. THESE DERIVED GATES OFFER MORE COMPLEX FUNCTIONALITIES AND ARE WIDELY USED IN DIGITAL CIRCUIT DESIGN.

### THE NAND GATE

THE NAND GATE (NOT AND) PERFORMS THE OPPOSITE FUNCTION OF AN AND GATE. IT OUTPUTS A LOW (0) ONLY IF ALL OF ITS INPUTS ARE HIGH. IN ALL OTHER CASES, ITS OUTPUT IS HIGH (1). THIS MEANS A NAND GATE PRODUCES A TRUE OUTPUT UNLESS ALL INPUTS ARE TRUE. NAND GATES ARE SIGNIFICANT BECAUSE THEY ARE "UNIVERSAL GATES," MEANING ANY OTHER LOGIC GATE CAN BE CONSTRUCTED USING ONLY NAND GATES.

### THE NOR GATE

SIMILARLY, THE NOR GATE (NOT OR) PERFORMS THE OPPOSITE OF AN OR GATE. IT OUTPUTS A HIGH (1) ONLY IF ALL OF ITS INPUTS ARE LOW. IF ANY INPUT IS HIGH, THE OUTPUT WILL BE LOW. LIKE NAND GATES, NOR GATES ARE ALSO UNIVERSAL GATES.

### THE XOR GATE (EXCLUSIVE OR)

THE XOR GATE OUTPUTS A HIGH (1) IF ITS INPUTS ARE DIFFERENT. IF THE INPUTS ARE THE SAME (BOTH 0 OR BOTH 1), THE OUTPUT IS LOW (0). THIS IS USEFUL FOR DETECTING DIFFERENCES BETWEEN TWO BINARY NUMBERS. FOR EXAMPLE, IN BINARY ADDITION, THE XOR GATE IS USED TO CALCULATE THE SUM BIT WITHOUT CONSIDERING THE CARRY.

### THE XNOR GATE (EXCLUSIVE NOR)

THE XNOR GATE IS THE INVERSE OF THE XOR GATE. IT OUTPUTS A HIGH (1) IF ITS INPUTS ARE THE SAME AND A LOW (0) IF ITS INPUTS ARE DIFFERENT. THIS IS OFTEN USED FOR COMPARISON PURPOSES OR IN PARITY CHECKING CIRCUITS.

# IMPLEMENTING LOGIC GATES

LOGIC GATES ARE PHYSICALLY REALIZED USING ELECTRONIC COMPONENTS, PRIMARILY TRANSISTORS, WHICH ACT AS TINY SWITCHES. THE WAY THESE TRANSISTORS ARE INTERCONNECTED DETERMINES THE SPECIFIC LOGIC FUNCTION PERFORMED BY THE GATE.

## TRANSISTORS AS SWITCHES

AT THE MOST BASIC LEVEL, TRANSISTORS, SUCH AS METAL-OXIDE-SEMICONDUCTOR FIELD-EFFECT TRANSISTORS (MOSFETs) OR BIPOLAR JUNCTION TRANSISTORS (BJTs), CAN BE CONFIGURED TO ACT AS ELECTRICALLY CONTROLLED SWITCHES. WHEN A VOLTAGE IS APPLIED TO THE CONTROL TERMINAL (GATE OR BASE), THE TRANSISTOR EITHER ALLOWS CURRENT TO FLOW (ON STATE, REPRESENTING 1) OR BLOCKS IT (OFF STATE, REPRESENTING 0). BY ARRANGING THESE TRANSISTORS IN SPECIFIC CONFIGURATIONS, DESIGNERS CAN CREATE CIRCUITS THAT IMPLEMENT THE AND, OR, NOT, AND OTHER LOGIC GATE FUNCTIONS.

## INTEGRATED CIRCUITS (ICs)

IN MODERN DIGITAL ELECTRONICS, LOGIC GATES ARE NOT BUILT INDIVIDUALLY BUT ARE MASS-PRODUCED WITHIN INTEGRATED CIRCUITS (ICs), OFTEN CALLED MICROCHIPS. AN IC IS A SMALL PIECE OF SEMICONDUCTOR MATERIAL, USUALLY SILICON, ON WHICH A COMPLEX NETWORK OF LOGIC GATES AND OTHER ELECTRONIC COMPONENTS IS FABRICATED. A SINGLE IC CAN CONTAIN MILLIONS OR EVEN BILLIONS OF TRANSISTORS, FORMING SOPHISTICATED LOGIC CIRCUITS THAT PERFORM COMPLEX OPERATIONS. THIS MINIATURIZATION AND INTEGRATION ARE WHAT ENABLE THE INCREDIBLE POWER AND DENSITY OF TODAY'S ELECTRONIC DEVICES.

## COMBINING LOGIC GATES: BUILDING COMPLEX CIRCUITS

THE TRUE POWER OF COMPUTER LOGIC GATES EMERGES WHEN THEY ARE INTERCONNECTED TO FORM LARGER, MORE COMPLEX DIGITAL CIRCUITS. THESE CIRCUITS PERFORM SPECIFIC COMPUTATIONAL TASKS, FROM SIMPLE ARITHMETIC TO COMPLEX DATA MANIPULATION.

## ADDERS

LOGIC GATES ARE FUNDAMENTAL TO BUILDING ARITHMETIC CIRCUITS, SUCH AS ADDERS. A HALF-ADDER, FOR INSTANCE, USES AN XOR GATE AND AN AND GATE TO ADD TWO SINGLE BINARY DIGITS AND PRODUCE A SUM AND A CARRY-OUT. A FULL-ADDER, WHICH HANDLES THREE INPUTS (TWO BITS AND A CARRY-IN), USES MORE GATES, INCLUDING OR GATES, TO PRODUCE A SUM AND A CARRY-OUT. MULTIPLE FULL-ADDERS CAN BE CHAINED TOGETHER TO CREATE RIPPLE-CARRY ADDERS OR CARRY-LOOKAHEAD ADDERS, ENABLING THE ADDITION OF MULTI-BIT BINARY NUMBERS.

## FLIP-FLOPS AND MEMORY

FLIP-FLOPS ARE FUNDAMENTAL MEMORY ELEMENTS IN DIGITAL ELECTRONICS, CAPABLE OF STORING A SINGLE BIT OF INFORMATION. THEY ARE CONSTRUCTED USING COMBINATIONS OF NAND OR NOR GATES, OFTEN CONFIGURED IN A LATCHED OR TOGGLED STATE. THESE FLIP-FLOPS FORM THE BASIS OF REGISTERS, COUNTERS, AND SEQUENTIAL LOGIC CIRCUITS, WHICH ARE ESSENTIAL FOR STORING DATA AND CONTROLLING THE FLOW OF OPERATIONS WITHIN A COMPUTER.

## MULTIPLEXERS AND DEMULTIPLEXERS

MULTIPLEXERS (MUX) ACT LIKE ELECTRONIC SWITCHES THAT SELECT ONE OF SEVERAL INPUT SIGNALS AND FORWARD IT TO A SINGLE OUTPUT LINE, BASED ON CONTROL SIGNALS. THEY ARE TYPICALLY BUILT USING AND, OR, AND NOT GATES. DEMULTIPLEXERS (DEMUX) PERFORM THE OPPOSITE FUNCTION, DIRECTING A SINGLE INPUT SIGNAL TO ONE OF SEVERAL OUTPUT LINES. BOTH ARE VITAL FOR DATA ROUTING AND SELECTION WITHIN COMPUTER SYSTEMS.

## THE IMPORTANCE OF LOGIC GATES IN COMPUTING

COMPUTER LOGIC GATES ARE THE INDISPENSABLE FOUNDATION OF ALL DIGITAL COMPUTING. EVERY OPERATION, FROM PROCESSING A SIMPLE COMMAND TO RUNNING COMPLEX SOFTWARE, IS ULTIMATELY BROKEN DOWN INTO A SERIES OF LOGICAL OPERATIONS PERFORMED BY THESE GATES. THE ABILITY TO MANIPULATE BINARY DATA THROUGH THESE FUNDAMENTAL CIRCUITS ALLOWS COMPUTERS TO PERFORM CALCULATIONS, MAKE DECISIONS, STORE INFORMATION, AND INTERACT WITH THE PHYSICAL WORLD. WITHOUT THE PRECISE AND RELIABLE FUNCTIONING OF LOGIC GATES, THE DIGITAL REVOLUTION THAT HAS TRANSFORMED OUR LIVES WOULD NOT HAVE BEEN POSSIBLE. THEIR SIMPLE, YET PROFOUND, ABILITY TO PROCESS BINARY INFORMATION IS THE CORE OF MODERN COMPUTATIONAL POWER.

## FREQUENTLY ASKED QUESTIONS

### WHAT ARE THE FUNDAMENTAL BUILDING BLOCKS OF DIGITAL CIRCUITS AND WHY ARE THEY CALLED 'LOGIC GATES'?

THE FUNDAMENTAL BUILDING BLOCKS OF DIGITAL CIRCUITS ARE CALLED LOGIC GATES. THEY ARE NAMED 'LOGIC GATES' BECAUSE THEY PERFORM BASIC LOGICAL OPERATIONS ON ONE OR MORE BINARY INPUTS (0s AND 1s) TO PRODUCE A SINGLE BINARY OUTPUT. THESE OPERATIONS CORRESPOND TO BOOLEAN ALGEBRA'S LOGICAL FUNCTIONS LIKE AND, OR, AND NOT.

### CAN YOU EXPLAIN THE FUNCTION OF THE AND GATE AND PROVIDE A SIMPLE REAL-WORLD ANALOGY?

AN AND GATE OUTPUTS A '1' (TRUE) ONLY IF ALL OF ITS INPUTS ARE '1'. OTHERWISE, IT OUTPUTS A '0' (FALSE). A REAL-WORLD ANALOGY IS A SERIES OF TWO LIGHT SWITCHES CONTROLLING A SINGLE LIGHT BULB: THE BULB ONLY TURNS ON IF BOTH SWITCHES ARE IN THE 'ON' POSITION. IF EITHER SWITCH IS 'OFF', THE BULB REMAINS OFF.

### WHAT IS THE CHARACTERISTIC BEHAVIOR OF AN OR GATE AND HOW IS IT DIFFERENT FROM AN AND GATE?

AN OR GATE OUTPUTS A '1' (TRUE) IF ANY OF ITS INPUTS ARE '1'. IT ONLY OUTPUTS A '0' (FALSE) IF ALL OF ITS INPUTS ARE '0'. THIS IS DIFFERENT FROM AN AND GATE, WHICH REQUIRES ALL INPUTS TO BE '1' FOR A '1' OUTPUT.

## How does a NOT gate (inverter) operate, and what is its primary purpose?

A NOT gate, also known as an inverter, has only one input and one output. Its output is always the opposite of its input. If the input is '1', the output is '0', and if the input is '0', the output is '1'. Its primary purpose is to invert the logical state of a signal.

## What are NAND and NOR gates, and why are they considered 'universal' gates?

NAND (NOT-AND) gates produce a '0' output only if all inputs are '1'. NOR (NOT-OR) gates produce a '1' output only if all inputs are '0'. They are considered 'universal' gates because any other logic gate (AND, OR, NOT, XOR, XNOR) can be constructed solely using combinations of NAND gates or solely using combinations of NOR gates.

## What is the function of an XOR (Exclusive OR) gate, and when is its output '1'?

An XOR gate outputs a '1' (true) if its inputs are different. It outputs a '0' (false) if its inputs are the same. For example, with two inputs, an XOR gate outputs '1' if one input is '0' and the other is '1', but outputs '0' if both inputs are '0' or both are '1'.

## How are logic gates implemented in modern computer hardware?

Logic gates are physically implemented using transistors. Transistors act as electrically controlled switches. Combinations of transistors are arranged to create the circuit configurations that perform the logical operations of each type of gate (AND, OR, NOT, etc.). These gate circuits are then fabricated on integrated circuits (ICs) or microchips.

## What is a truth table, and how is it used in understanding logic gates?

A truth table is a mathematical table that shows all possible combinations of inputs for a logic gate (or circuit) and the corresponding output for each combination. Truth tables are essential for precisely defining and verifying the behavior of logic gates and for designing and analyzing digital circuits.

## Beyond basic operations, what are some more complex digital circuits that utilize logic gates?

Logic gates are the foundation for more complex digital circuits such as adders (for arithmetic operations), multiplexers (for selecting data), decoders (for converting binary to other forms), flip-flops (for storing single bits of data, forming memory), and eventually entire microprocessors and memory units.

## Additional Resources

Here are 9 book titles related to computer logic gates, following your specifications:

### 1. *The Foundation of Digital Design: Logic Gates Explained*

This introductory book delves into the fundamental building blocks of digital electronics. It meticulously explains the operation of basic logic gates such as AND, OR, NOT, XOR, NAND, and NOR. Readers will grasp how these gates combine to perform complex logical operations, forming the basis of all digital circuits and computers.

### 2. *Inside the Integrated Circuit: A Gate-Level Perspective*

This title offers a deeper dive into how logic gates are physically implemented within integrated circuits. It explores the semiconductor physics and manufacturing processes that enable the creation of these essential

COMPONENTS. THE BOOK PROVIDES A MICROSCOPIC VIEW OF DIGITAL COMPUTATION, BRIDGING THE GAP BETWEEN ABSTRACT LOGIC AND PHYSICAL HARDWARE.

### 3. *BOOLEAN ALGEBRA FOR COMPUTER ARCHITECTS: MASTERING LOGIC*

THIS BOOK FOCUSES ON THE MATHEMATICAL UNDERPINNINGS OF LOGIC GATES, SPECIFICALLY BOOLEAN ALGEBRA. IT DEMONSTRATES HOW LOGICAL EXPRESSIONS CAN BE SIMPLIFIED, OPTIMIZED, AND MANIPULATED USING ALGEBRAIC PRINCIPLES. UNDERSTANDING BOOLEAN ALGEBRA IS CRUCIAL FOR DESIGNING EFFICIENT AND RELIABLE DIGITAL SYSTEMS, AND THIS TITLE GUIDES READERS THROUGH ITS APPLICATION IN COMPUTER ARCHITECTURE.

### 4. *FROM LOGIC TO MICROPROCESSORS: THE EVOLUTION OF COMPUTING*

TRACING THE HISTORICAL DEVELOPMENT OF COMPUTING, THIS BOOK HIGHLIGHTS THE PIVOTAL ROLE OF LOGIC GATES. IT ILLUSTRATES HOW EARLY BREAKTHROUGHS IN LOGIC DESIGN LED TO THE CREATION OF MORE SOPHISTICATED COMPUTING DEVICES, ULTIMATELY CULMINATING IN MODERN MICROPROCESSORS. THE NARRATIVE EMPHASIZES THE ITERATIVE PROCESS OF INNOVATION DRIVEN BY ADVANCEMENTS IN LOGIC IMPLEMENTATION.

### 5. *DESIGNING DIGITAL SYSTEMS WITH COMBINATIONAL LOGIC*

THIS PRACTICAL GUIDE CENTERS ON THE DESIGN OF COMBINATIONAL LOGIC CIRCUITS, WHICH ARE BUILT EXCLUSIVELY FROM LOGIC GATES WHOSE OUTPUTS ARE SOLELY DEPENDENT ON CURRENT INPUTS. IT COVERS TECHNIQUES FOR ANALYZING AND SYNTHESIZING COMBINATIONAL CIRCUITS FOR VARIOUS APPLICATIONS, FROM SIMPLE ARITHMETIC UNITS TO COMPLEX MULTIPLEXERS. THE BOOK PROVIDES HANDS-ON EXAMPLES AND METHODOLOGIES FOR CREATING FUNCTIONAL DIGITAL SYSTEMS.

### 6. *SEQUENTIAL LOGIC AND FINITE STATE MACHINES: THE MEMORY OF CIRCUITS*

MOVING BEYOND COMBINATIONAL LOGIC, THIS TITLE EXPLORES SEQUENTIAL LOGIC CIRCUITS, WHICH INCORPORATE MEMORY ELEMENTS LIKE FLIP-FLOPS AND LATCHES. IT EXPLAINS HOW THESE CIRCUITS, CONSTRUCTED FROM LOGIC GATES, ENABLE DIGITAL SYSTEMS TO STORE INFORMATION AND EXHIBIT TIME-DEPENDENT BEHAVIOR. THE BOOK INTRODUCES THE CONCEPT OF FINITE STATE MACHINES, A POWERFUL TOOL FOR DESIGNING SEQUENTIAL SYSTEMS.

### 7. *LOGIC GATE SIMULATION AND VERIFICATION: ENSURING CORRECTNESS*

THIS BOOK ADDRESSES THE CRITICAL PROCESSES OF SIMULATING AND VERIFYING DIGITAL CIRCUITS BUILT WITH LOGIC GATES. IT INTRODUCES VARIOUS SOFTWARE TOOLS AND METHODOLOGIES USED TO TEST THE FUNCTIONALITY AND PERFORMANCE OF LOGIC DESIGNS BEFORE THEY ARE MANUFACTURED. ENSURING THE CORRECTNESS OF LOGIC GATE OPERATIONS IS PARAMOUNT IN PREVENTING ERRORS IN COMPLEX DIGITAL SYSTEMS.

### 8. *THE ART OF LOGIC SYNTHESIS: OPTIMIZING GATE NETWORKS*

THIS TITLE DELVES INTO THE ADVANCED FIELD OF LOGIC SYNTHESIS, WHICH FOCUSES ON AUTOMATICALLY TRANSFORMING HIGH-LEVEL HARDWARE DESCRIPTIONS INTO OPTIMIZED GATE-LEVEL NETLISTS. IT EXPLORES ALGORITHMS AND TECHNIQUES FOR MINIMIZING THE NUMBER OF LOGIC GATES, REDUCING POWER CONSUMPTION, AND IMPROVING CIRCUIT SPEED. THE BOOK SHOWCASES HOW SOPHISTICATED TOOLS LEVERAGE LOGIC GATE PRINCIPLES FOR EFFICIENT CIRCUIT DESIGN.

### 9. *QUANTUM LOGIC GATES: THE FUTURE OF COMPUTATION*

THIS FORWARD-LOOKING BOOK INTRODUCES THE EMERGING FIELD OF QUANTUM COMPUTING AND ITS RELIANCE ON QUANTUM LOGIC GATES. IT EXPLAINS HOW THESE NOVEL GATES, OPERATING ON QUBITS, DIFFER FROM CLASSICAL LOGIC GATES AND ENABLE ENTIRELY NEW COMPUTATIONAL PARADIGMS. READERS WILL GAIN AN UNDERSTANDING OF THE FUNDAMENTAL PRINCIPLES THAT UNDERPIN THE POTENTIAL OF QUANTUM COMPUTATION.

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